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P-CAD PCB User's Guide i

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implementation. The book emphasizes practical design issues - from high-level behavioural modelling in MATLAB/SIMULINK, to circuit-level implementation in Cadence Design Framework II. As well as being a

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flow of sigma-delta ADCs. The book begins with an introductory survey of sigma-deltamodulators, their fundamentals architectures and synthesis methodscovered in Chapter 1. In Chapter 2, the effect of main

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circuiterror mechanisms is analysed, providing the necessary understanding of the main practical issues affecting the performance of sigma-delta modulators. The knowledge derived from the first two chapters is

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presented in the book as an essential part of the systematic top-down/bottom-up synthesis methodology of sigma-delta modulators described in Chapter 3, where a time-domain behavioural simulator

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named SIMSIDES is described and applied to the high-level design and verification of sigma-delta ADCs. Chapter 4 moves farther down from system-level to the circuit and physical level, providing a number of design

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recommendations and practical recipes to complete the design flow of sigma-delta modulators. To conclude the book, Chapter 5 gives an overview of the state-of-the-art sigma-delta ADCs, which are exhaustively

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analysed in order to extract practical design guidelines and to identify the incoming trends, design challenges as well as practical solutions proposed by cutting-edge designs. Offers a complete

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